PATENT NUMBER and ISSUE DATE

U.S. **UTILITY** Patent Application

| APPL NUM | FILING DATE | CLASS | SUBCLASS | GAU | EXAMINER |
|---|---|---|---|---|---|
| 10083163 | 02/27/2002 | ~~257~~ ~~438~~ | 377 | ~~2826~~ ~~2812~~ | Dickey |

**APPLICANTS: Hokazono Akira; TH

**CONTINUING DATA VERIFIED:

Now TH

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-392569 12/25/2001 TH

PG-PUB DO NOT PUBLISH ☐ RESCIND ☐

Foreign priority claimed ☒ yes ☐ no
35 USC 119 conditions met ☒ yes ☐ no
Verified and Acknowledged Examiners's intials

ATTORNEY DOCKET NO
220110US2S

TITLE : Semiconductor device having active regions connected together by interconnect layer and method of manufacture thereof

U.S.DEPT. OF COMM./PAT.& TM-PTO-436L(Rev. 12-94)

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|---|---|---|
| NOTICE OF ALLOWANCE MAILED | Assistant Examiner | CLAIMS ALLOWED: Total Claims / Print Claim for O.G |
| ISSUE FEE: Amount Due / Date Paid | Primary Examiner | DRAWING: Sheets Drwg. / Figs.Drwg. / Print Fig. |
| ☐ TERMINAL DISCLAIMER | PREPARED FOR ISSUE | Application Examiner |



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